

Notice of References Cited	Application/Control No. 09/746,523	Applicant(s)/Patent Under Reexamination HOOPER ET AL.	
	Examiner Ted T. Vo	Art Unit 2122	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,659,749	08-1997	Mitchell et al.	709/108
	B	US-5,815,727	09-1998	Motomura, Masato	712/1
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Schulz et al., "A thread-aware debugger with an open interface", ACM, pages: 201-211, August 2000.
	V	Xu et al., "Dynamic Instrumentation of Thread Applications", ACM, pages: 1-11, May 1999.
	W	Hardware Reference Manual, "Intel™ IXP1200 Network Processor Hardware Reference Manual", http:// www.hcs.ufl.edu/ , version 1.0, pages: 4.1 - 4.50, June 2000.
	X	Jacobson et al., "Asynchronous Microengines for Efficient High Level Control", IEEE, pages: 201-218, 1997.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.